

10/535064

JC1 Rec'd PCT/PTO 13 MAY 2003

FORM PTO-1449/A and B (Modified)  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				APPLICATION NO.: Unassigned		ATTY. DOCKET NO.: S1022.81244US00	
				FILING DATE: Herewith		CONFIRMATION NO.: Unassigned	
				APPLICANT: Xavier Robert			
				GROUP ART UNIT: Unassigned		EXAMINER: Unassigned	
Sheet	1	of	1				

## U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
VX/H		5,724,505		Argade et al.	03/03/1998
VX/H		5,848,264		Baird et al.	12/08/1998
VX/H		5,974,573		Martin	10/26/1999
VX/H		6,052,774		Segars et al.	04/18/2000
VX/H		2002/0013893	A1	Roy et al.	01/31/2002

## FOREIGN PATENT DOCUMENTS

Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/Country	Number	Kind Code			
VX/H		EP	1184790	A	Mann	03/06/02	

## OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
VX/H		International Search Report from corresponding International Application No. PCT/FR2002/003996, filed November 21, 2002	
VX/H		International Search report from a related International Application No. PCT/FR2002/003908, filed November 14, 2002	
VX/H		International Search report from a related International Application No. PCT/FR2002/003909, filed November 14, 2002	
VX/H		Nexis 5001 Forum: <u>Standard for a Global Embedded Processor Debug Interface</u> , December 15, 1999, IEEE-ISTO XP002247195	

EXAMINER: <i>Allen Vo</i>	DATE CONSIDERED: <i>28/9/06</i>
------------------------------	------------------------------------

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_, filed \_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC 371 after June 30, 2003. See 37 CFR 1.491(b). For all patent applications filed on or before June 30, 2003, copies of cited U.S. patents and patent application publications are still required unless an eIDS is filed. Copies of all other patent(s), publication(s), or other information listed must still be provided, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]